WHAT IS CLAIMED IS:

1. An information processing device which specifies a waiting time until execution of a given event and makes a system call, the device comprising:

a first timer circuit which is set for a first cycle;

a second timer circuit which is set for a second cycle which is shorter than the first cycle;

a timeout supervisor which can store the waiting time when the system call is made; and

a first cycle supervisor which can store a time until the next interruption request from the first timer circuit when the system call is made,

wherein the timeout supervisor stores the time as a result of subtraction of the time stored in the first cycle supervisor from the time stored in the timeout supervisor upon an interruption request from the first timer; and if the time stored in the timeout supervisor is shorter than the first cycle, the second cycle time is subtracted from the time stored in the timeout supervisor upon an interruption request from the second timer circuit.

2. The information processing device according to Claim 1, wherein if the time stored in the timeout supervisor is longer than the first cycle, an interruption request from

the second timer circuit is disabled, and if the time stored in the timeout supervisor is shorter than the first cycle, an interruption request from the second timer is enabled.

3. The information processing device according to Claim 2, wherein the first cycle supervisor enables input of the time duration of the first cycle.

4. The information processing device according to Claim 3, wherein if the result of subtraction of the second cycle value from the value in the timeout supervisor is zero or less, the given event is executed.

5. The information processing device according to Claim 1, wherein the first timer and the second timer operate in accordance with the same clock, and the first cycle is an integral multiple of the second cycle.

6. An information processing device which measures a given time from a system call until execution of a given event, the device comprising:

a first timer which counts at intervals of a first cycle; and

a second timer which counts at intervals of a second cycle which is shorter than the first cycle,

wherein, when measuring the given time, the first timer counts and if the time until execution of the given event is shorter than the first cycle, the second timer counts.

7. The information processing device according to Claim 6, wherein the information processing device is a mobile terminal.

8. The information processing device according to Claim 6, wherein the first cycle is a duration of 10 milliseconds and the second cycle is a duration of 1 millisecond.

9. The information processing device according to Claim 6, wherein the first cycle and the second cycle can be respectively set on the first timer and the second timer from outside.

10. The information processing device according to Claim 6, further comprising a timeout supervisor which measures the given time,

wherein the given time is measured by counting down the time stored in the timeout supervisor upon an interruption request from the first timer, and when the remainder time until the given time becomes smaller than the first cycle,

counting down the time stored in the timeout supervisor upon an interruption request from the second timer.

11. The information processing device according to Claim 10, wherein the device has an operating system and measurement of the given time is made according to the operating system.

12. An information processing device which executes a given event a given time after a system call, the device comprising:

a central processing unit;

a memory connected with the central processing unit;

a first timer which issues an interruption request on the basis of pulses of a first cycle;

a second timer which issues an interruption request on the basis of pulses of a second cycle which is shorter than the first cycle; and

a timeout supervisor which stores the given time upon the system call,

wherein the central processing unit changes the time stored in the timeout supervisor upon an interruption request from the first timer, and when the time stored in the timeout supervisor is shorter than the time interval of the first cycle, changes the time stored in the timeout

supervisor upon an interruption request from the second timer.

13. The information processing device according to Claim 12, wherein the central processing unit counts down the time stored in the timeout supervisor upon an interruption request from the first timer or the second timer, and when the time stored in the timeout supervisor becomes zero or less, the given event is executed.

14. The information processing device according to Claim 13, wherein the memory includes an operating system and the timeout supervisor is created in the memory as a function of the operating system.

15. The information processing device according to Claim 14, wherein the information processing device is a mobile terminal.

16. The information processing device according to Claim 15, wherein the first cycle is a duration of 10 milliseconds and the second cycle is a duration of 1 millisecond.